WHAT IS CLAIMED IS:

1. A method for directing control of communication signals in a concatenated payload in a communication circuit, the method comprising:

receiving a multiplex order of the concatenated payload in M communication signals;

dividing the M communication signals by three to determine a number Y;

determining the control of the M communication signals by:

designating the first signal of the M communication signals as a control signal;

designating the second signal through a Yth signal of the M communication signals as being controlled by the immediately preceding signal thereto; and

designating each Y+1th signal of the M signals through the Mth communication signal as being controlled by a signal Y positions prior thereto.

2. The method of claim 1 wherein the communication signals are synchronous transport signals.

3. The method of claim 1 wherein the M communication signals are in a multiplexed order.

4. The method of claim 1 wherein the first signal of the M communication signals is a control signal read and write capability for frequency difference buffering using increment/decrement technology.

5. The method of claim 1 wherein M is one of 1, 24, 48, 96, 192, 768, and 3072.

6. The method of claim 1 wherein M is a multiple of three and two and is further greater than or equal to twelve.

7. The method of claim 1 wherein the communication circuit is disposed on a router.

8. The method of claim 1 wherein the communication circuit is disposed on an application specific integrated circuit (ASIC).

9. The method of claim 1 wherein the concatenated payload includes one or more of at least one of an STS-1, an STS-3, an STS-48, an STS-12, an STS-24, and an STS-X, wherein X is a multiple of three.

10. A communication circuit for directing control of communication signals in a concatenated payload, the apparatus comprising:

a module configured to receive a multiplex order of the concatenated payload in M communication signals;

a module configured to divide the M communication signals by three to determine a number Y;

a module configured to control of the M communication signals by:

designating the first signal of the M communication signals as a control signal;

designating the second signal through a Yth signal of the M communication signals as being controlled by the immediately preceding signal thereto; and

designating each Y+1th signal of the M signals through the Mth communication signal as being controlled by a signal Y positions prior thereto.

11. The communication circuit of claim 10 wherein the communication signals are synchronous transport signals.

12. The communication circuit of claim 10 wherein the M communication signals are in a multiplexed order.

13. The communication circuit of claim 10 wherein the first signal of the M communication signals is a control signal read and write capability for frequency difference buffering using increment/decrement technology.

14. The communication circuit of claim 10 wherein M is one of 1, 24, 48, 96, 192, 768, and 3072.

15. The communication circuit of claim 10 wherein M is a multiple of three and two and is further greater than or equal to twelve.

16. The communication circuit of claim 10 wherein the communication circuit is disposed on a router.

17. The communication circuit of claim 10 wherein the communication circuit is an application specific integrated circuit (ASIC).

18. The communication circuit of claim 10 wherein the concatenated payload includes one or more of at least one of an STS-1, an STS-3, an STS-48, an STS-12, an STS-24, and an STS-X, wherein X is a multiple of three

19. A computer program product for directing control of communication signals in a concatenated payload, the computer program product comprising:

signal bearing media bearing programming adapted to:

receive a multiplex order of the concatenated payload in M communication signals;

divide the M communication signals by three to determine a number Y;

control the M communication signals by:

designating the first signal of the M communication signals as a control signal;

designating the second signal through a Yth signal of the M communication signals as being controlled by the immediately preceding signal thereto; and

designating each Y+1th signal of the M signals through the Mth communication signal as being controlled by a signal Y positions prior thereto.

20. The computer program product of claim 19, wherein said signal bearing media is transmission media.

21. The computer program product of claim 19, wherein said signal bearing media is recordable media.

22. A communication system for directing control of communication signals in a concatenated payload in a communication circuit, the communication system comprising:

means for receiving a multiplex order of the concatenated payload in M communication signals;

means for dividing the M communication signals by three to determine a number Y;

means for determining the control of the M communication signals implemented with:

means for designating the first signal of the M communication signals as a control signal;

means for designating a second signal through a Yth signal of the M communication signals as being controlled by the immediately preceding signal thereto; and

means for designating each Y+1th signal of the M signals through the Mth communication signal as being controlled by a signal Y positions prior thereto.

23. The communication system of claim 22 wherein the communication signals are synchronous transport signals.

24. The communication system of claim 22 wherein the M communication signals are in a multiplexed order.

25. The communication system of claim 22 wherein the first signal of the M communication signals is a control signal read and write capability for frequency difference buffering using increment/decrement technology.

26. The communication system of claim 22 wherein M is one of 1, 24, 48, 96, 192, 768, and 3072.

27. The communication system of claim 22 wherein M is a multiple of three and two and is further greater than or equal to twelve.

28. The communication system of claim 22 wherein the communication circuit is disposed on a router.

29. The communication system of claim 22 wherein the communication circuit is disposed on an application specific integrated circuit (ASIC).

30. The communication system of claim 22 wherein the concatenated payload includes one or more of at least one of an STS-1, an STS-3, an STS-48, an STS-12, an STS-24, and an STS-X, wherein X is a multiple of three.